

FIG. 1

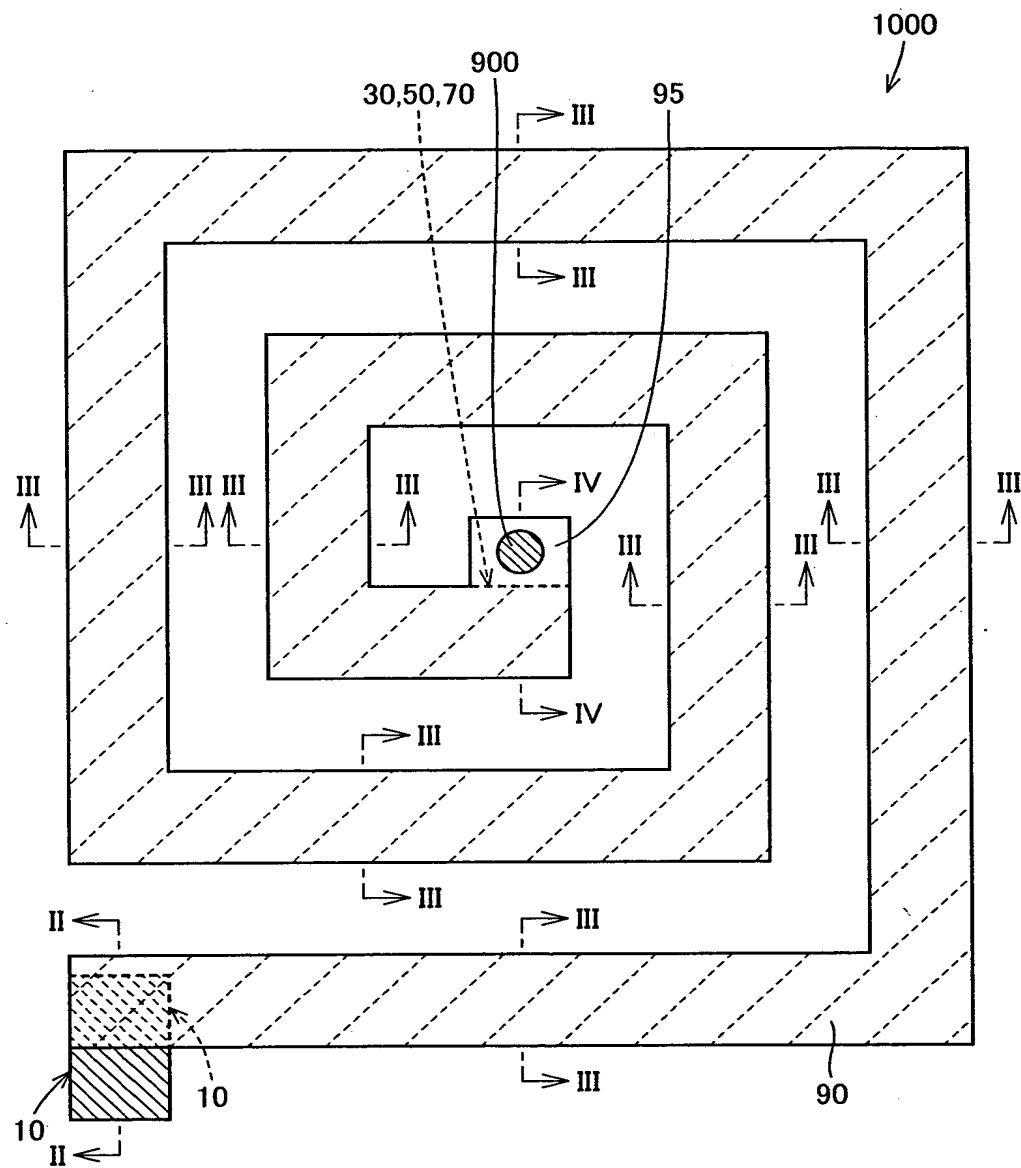


FIG.2

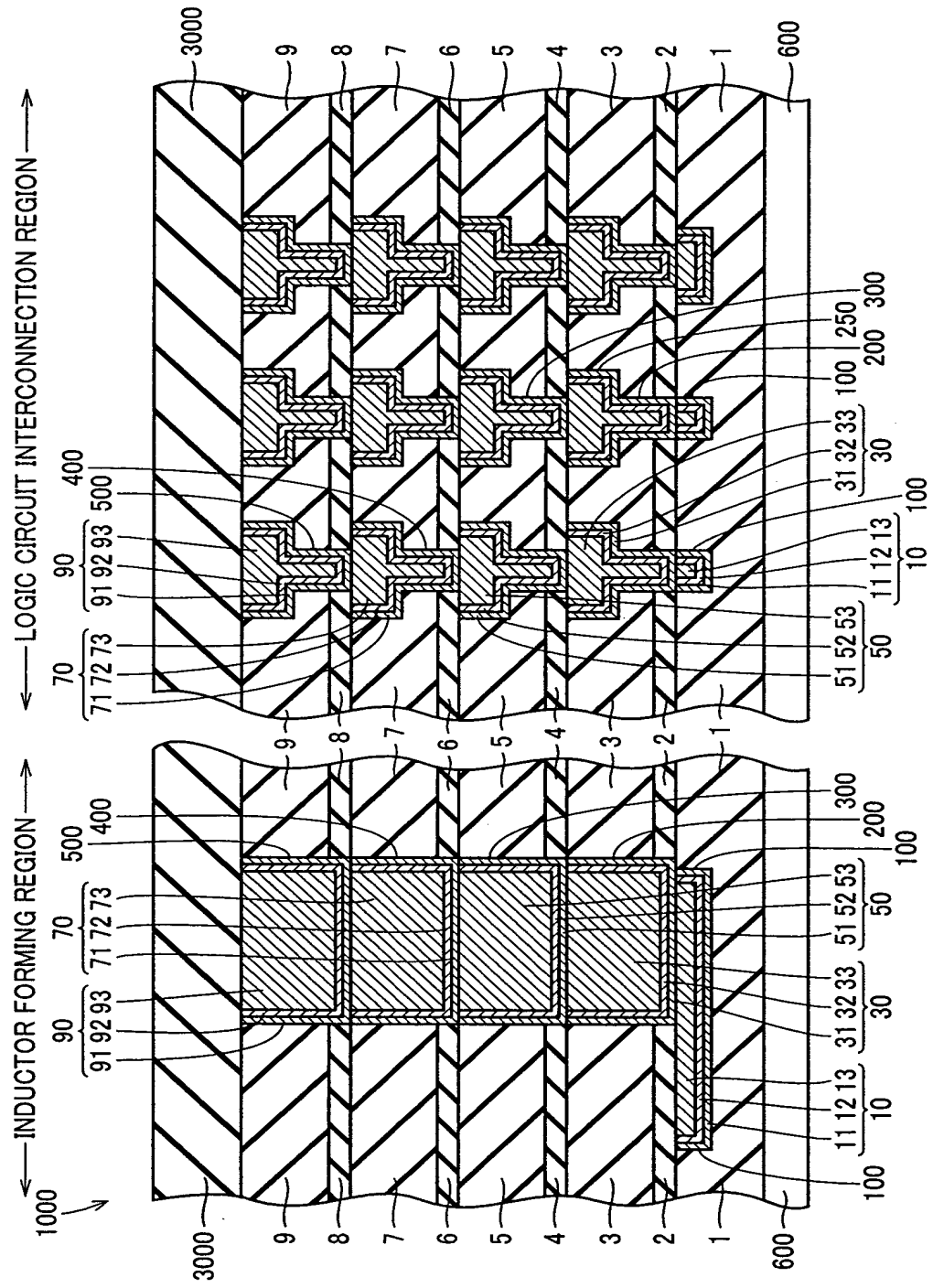


FIG.3

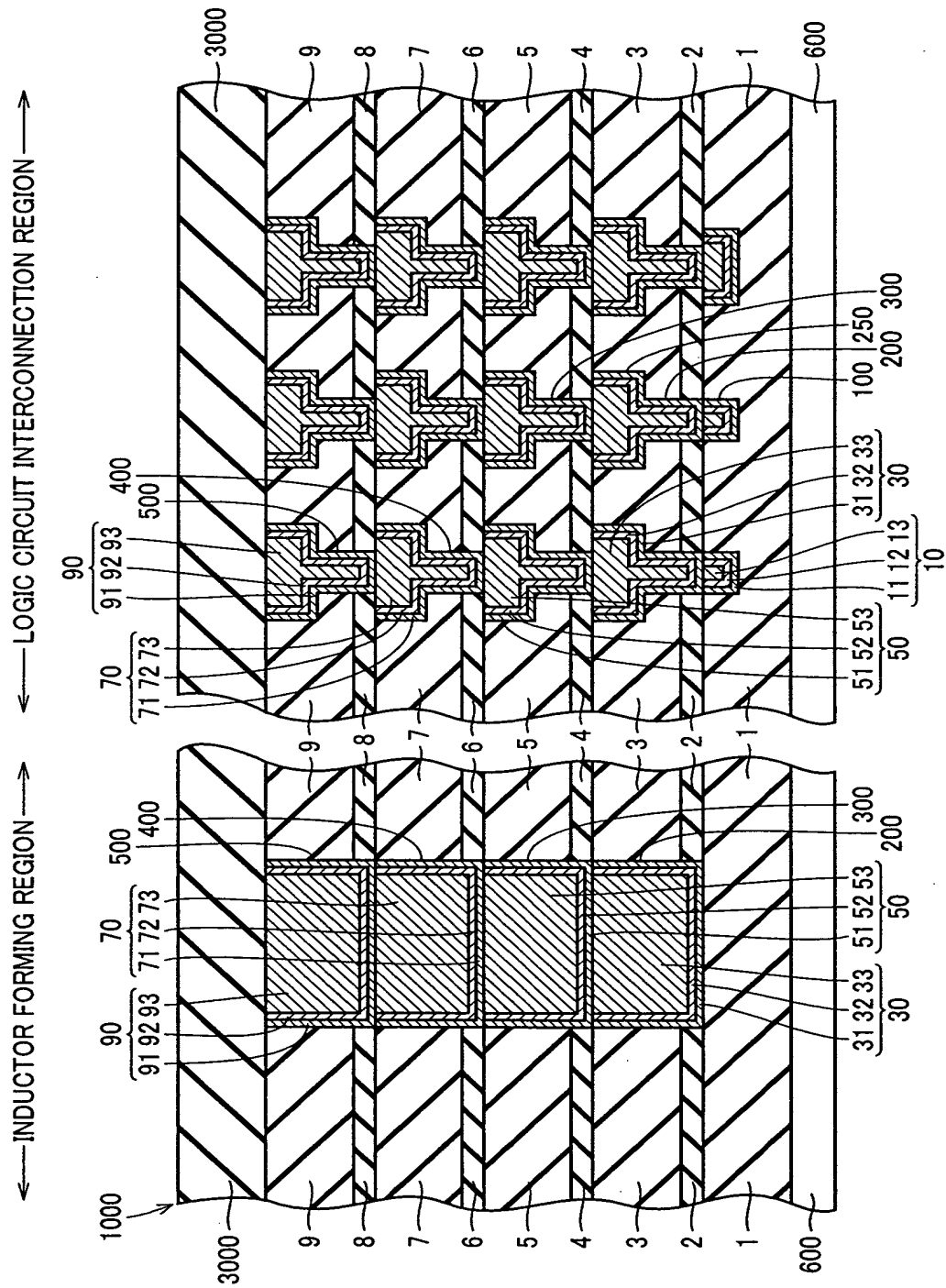


Figure 3.4 is a cross-sectional view of a multilayer printed circuit board (PCB) showing two distinct regions: an inductor forming region on the left and a logic circuit interconnection region on the right. The board is composed of multiple layers, with the top layer labeled 1 and the bottom layer labeled 9. The inductor forming region features a central inductor structure with a height 'H' and a width of 900. The logic circuit interconnection region shows a complex pattern of interconnections. Dimensions are provided for various layers and regions, including a central inductor area with a height 'H' and a width of 900. The logic circuit interconnection region is shown on the right side of the board.

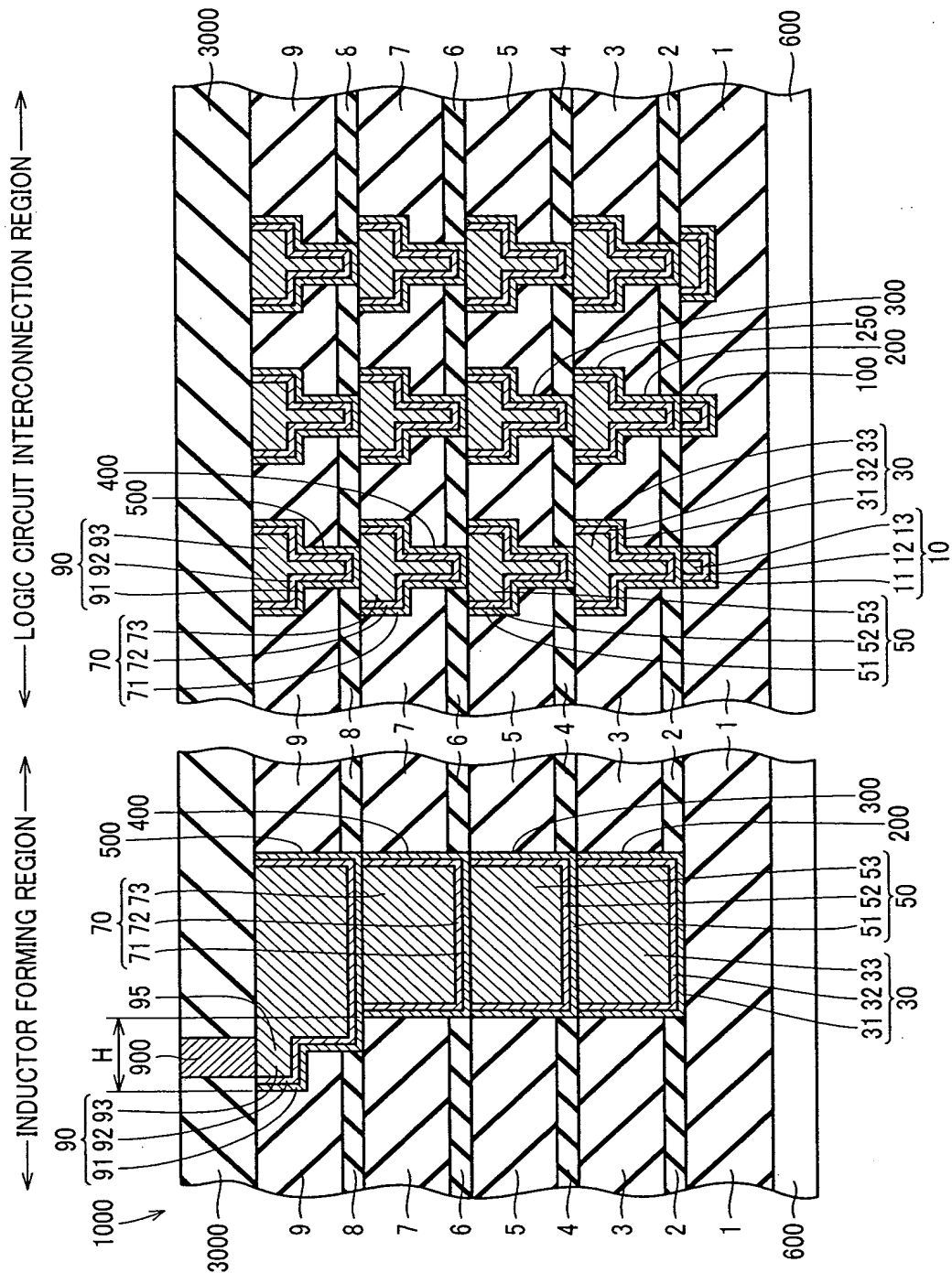


FIG.5

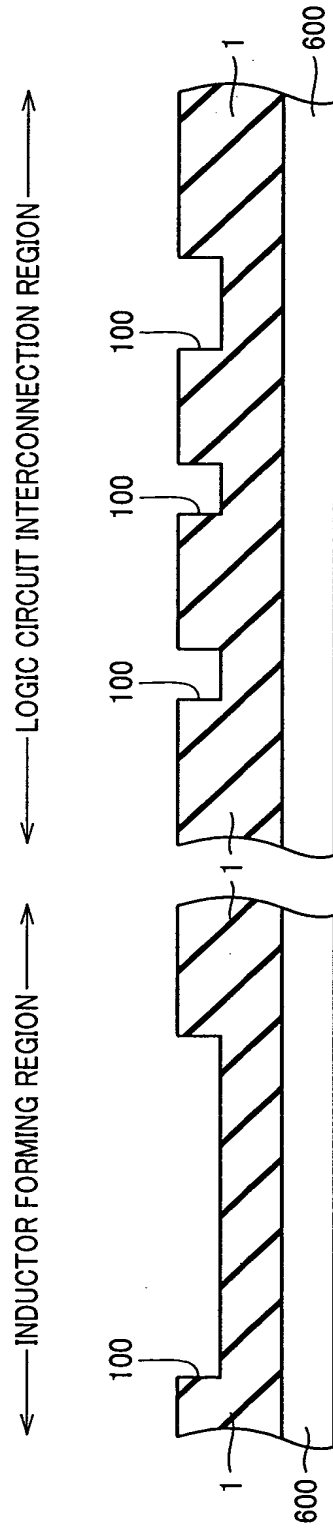


FIG.6

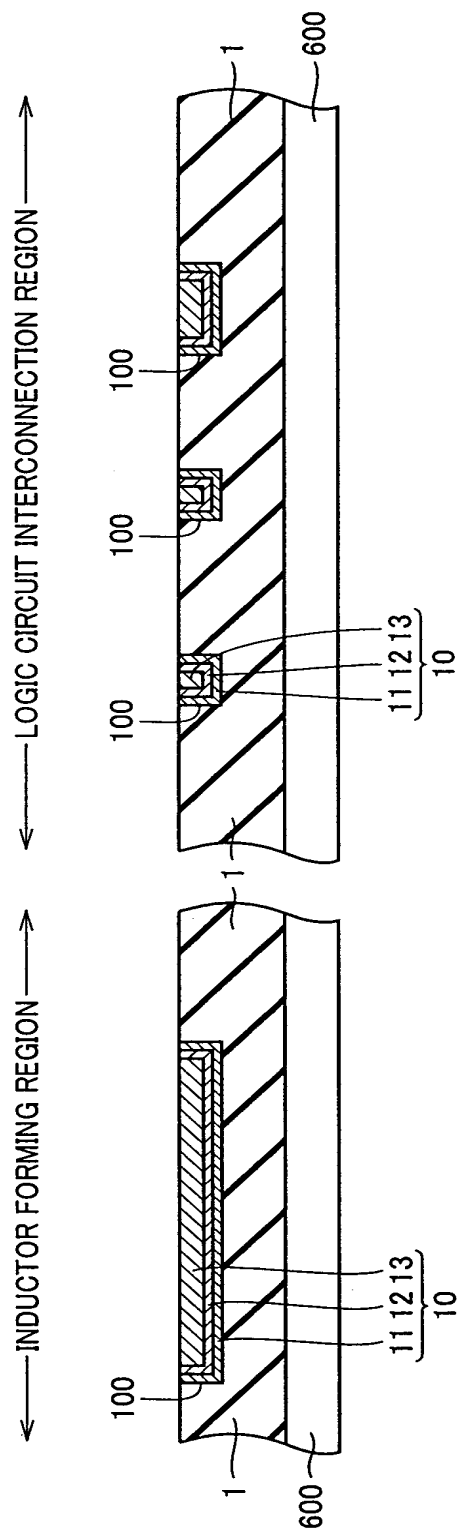


FIG.7

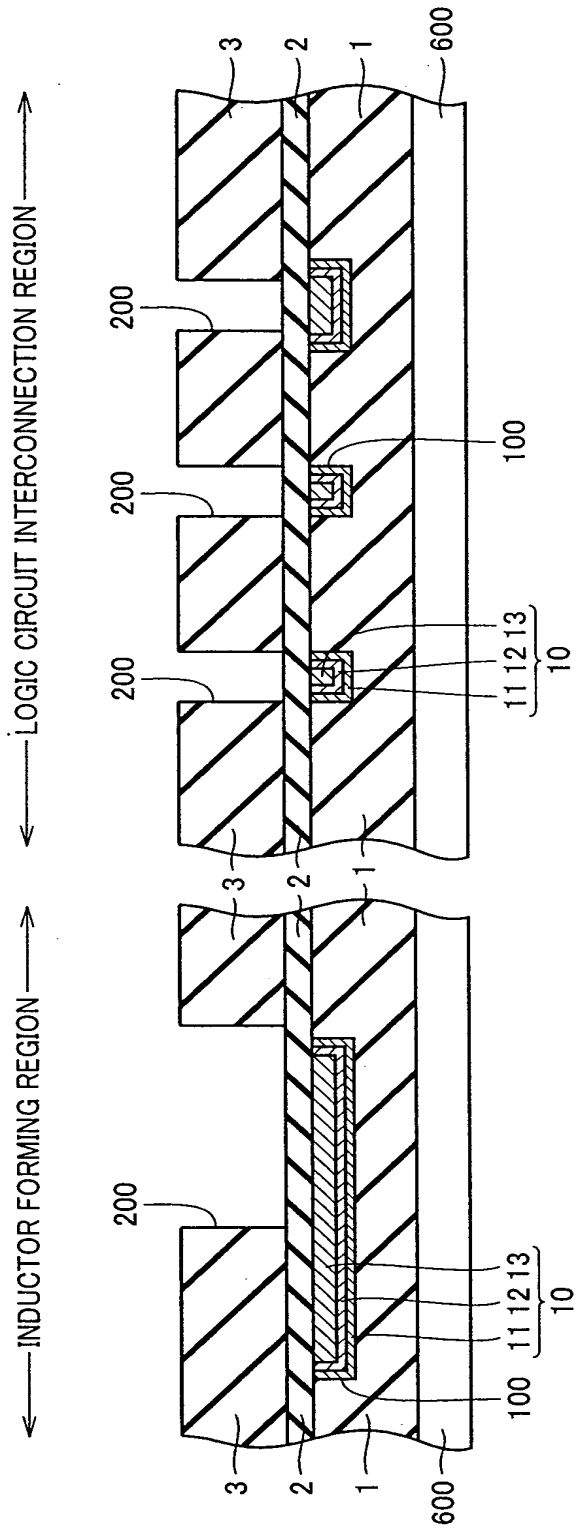


FIG.8

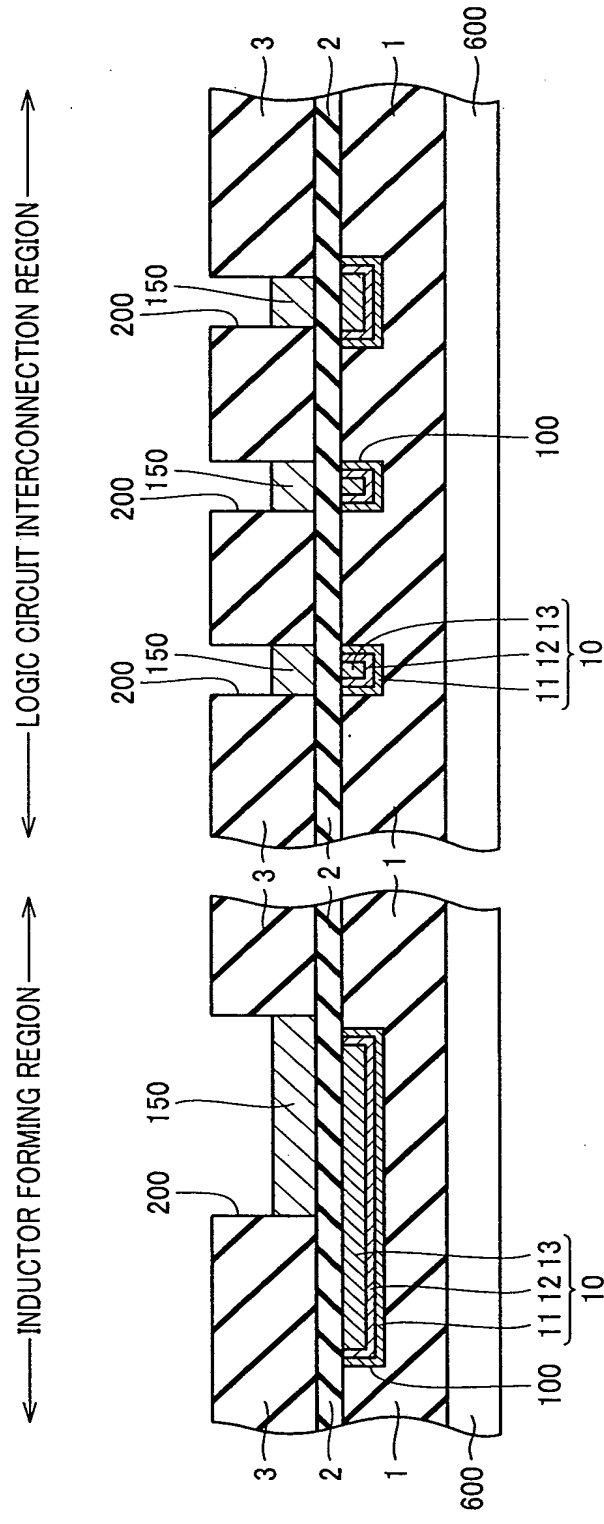




FIG.9

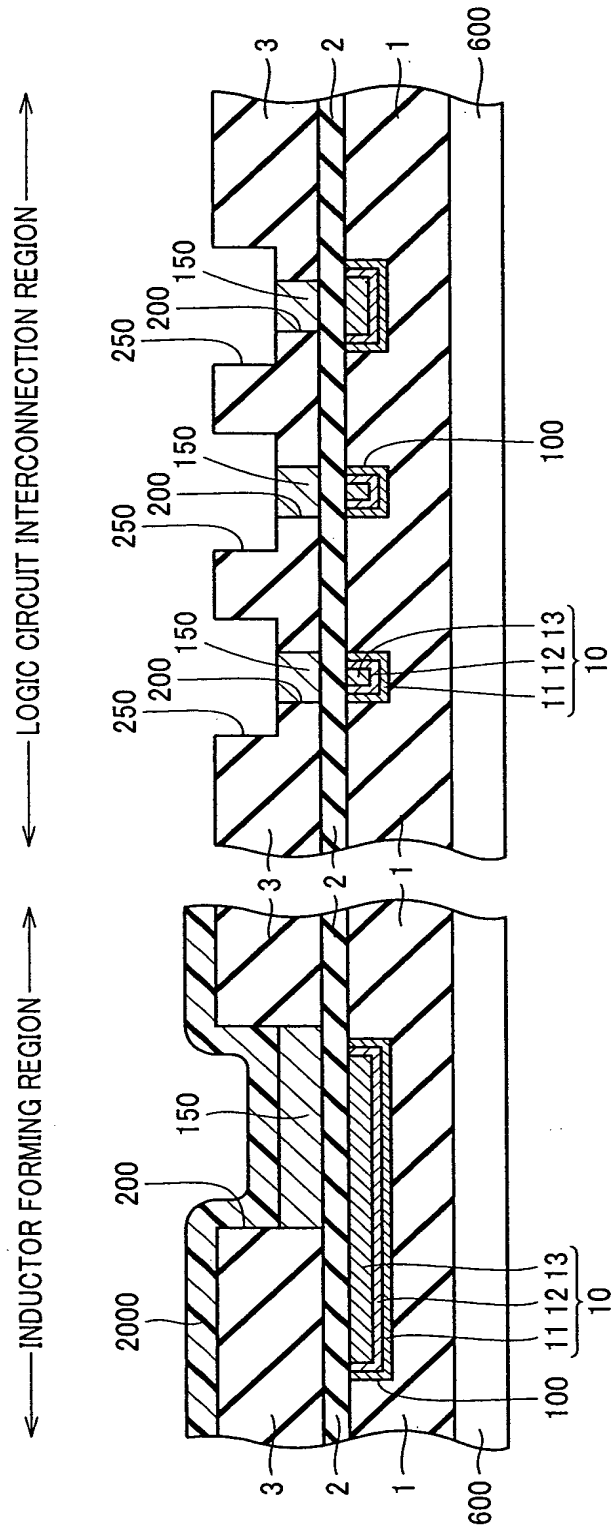


FIG.10

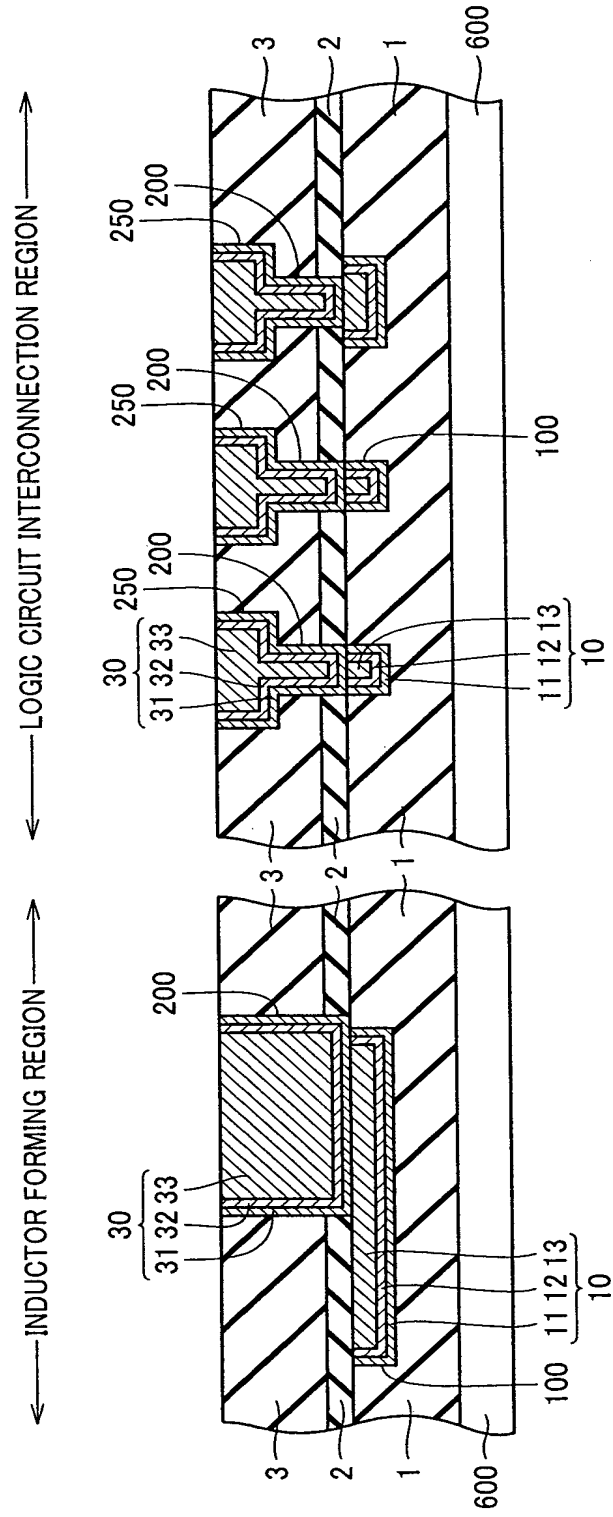


FIG.11

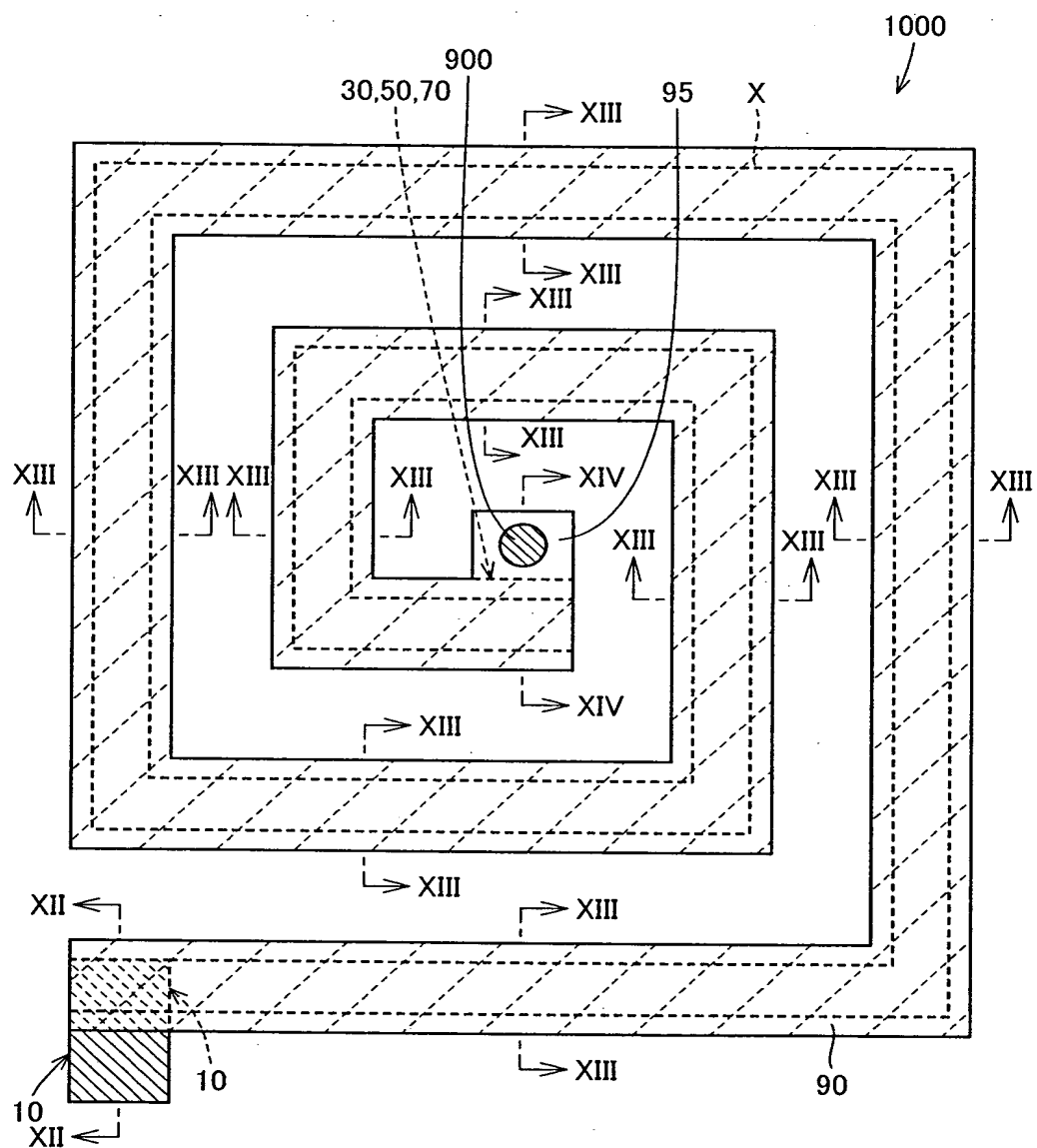


FIG.12

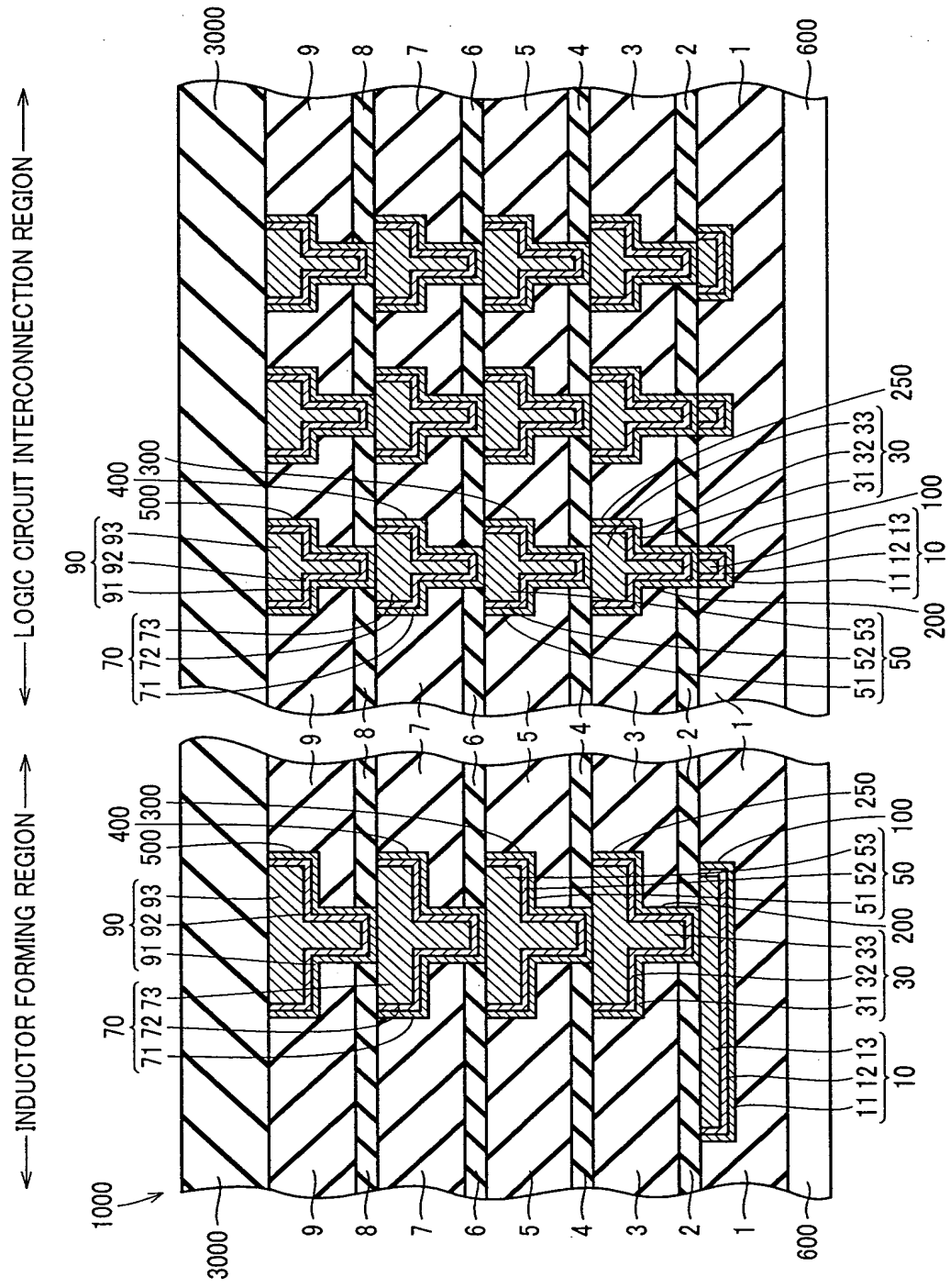


FIG.13

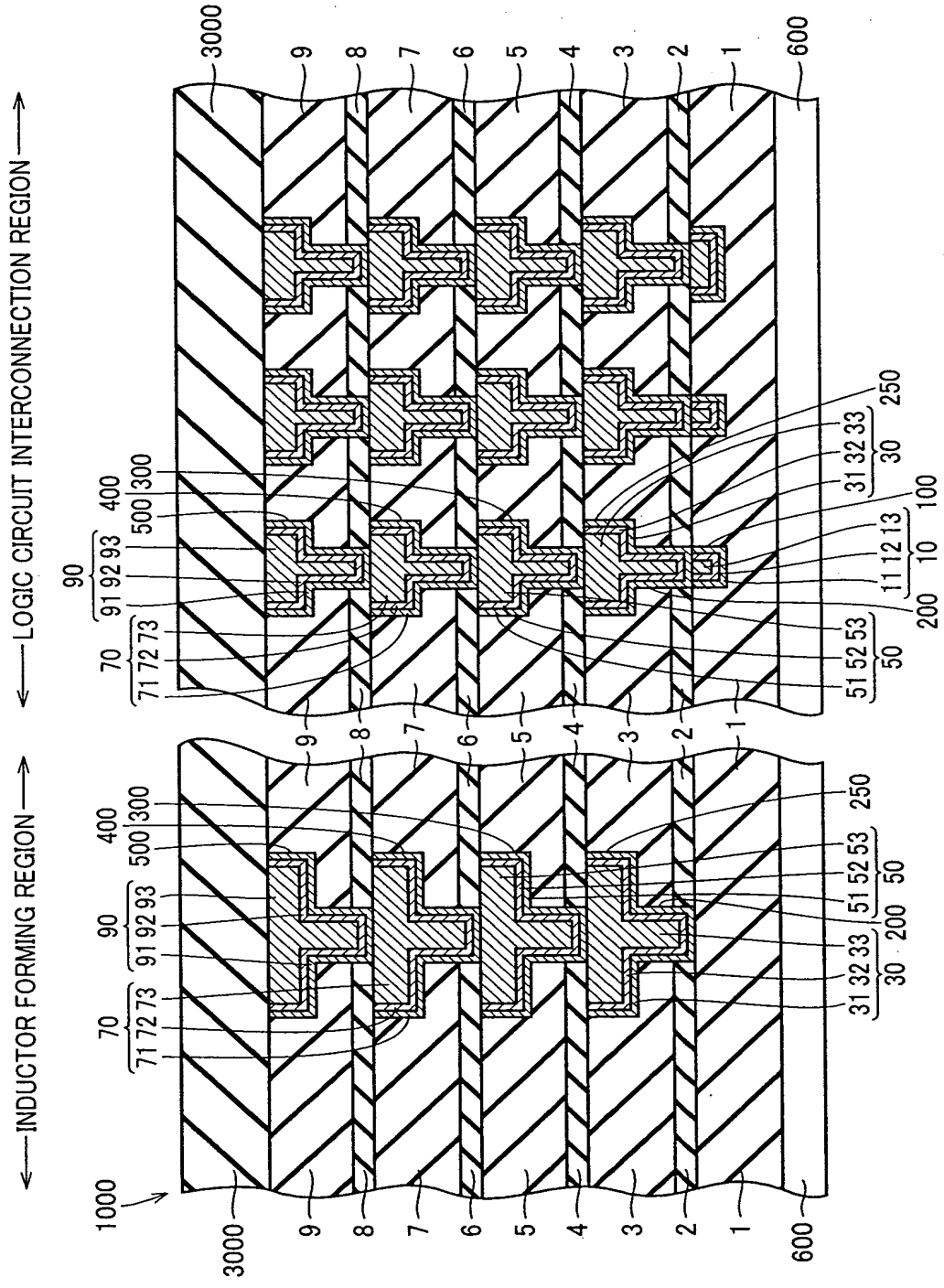


FIG.14

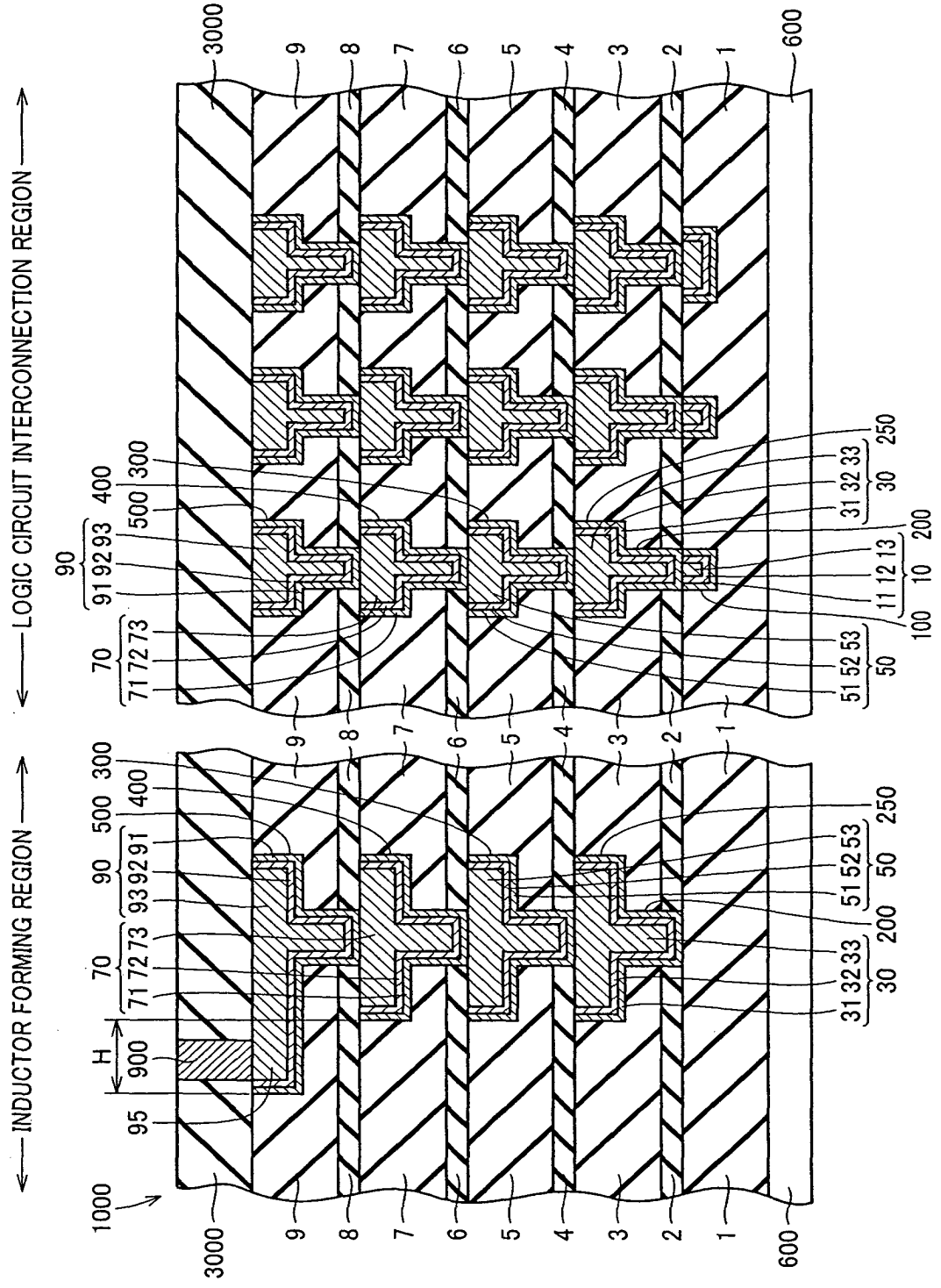


FIG.15

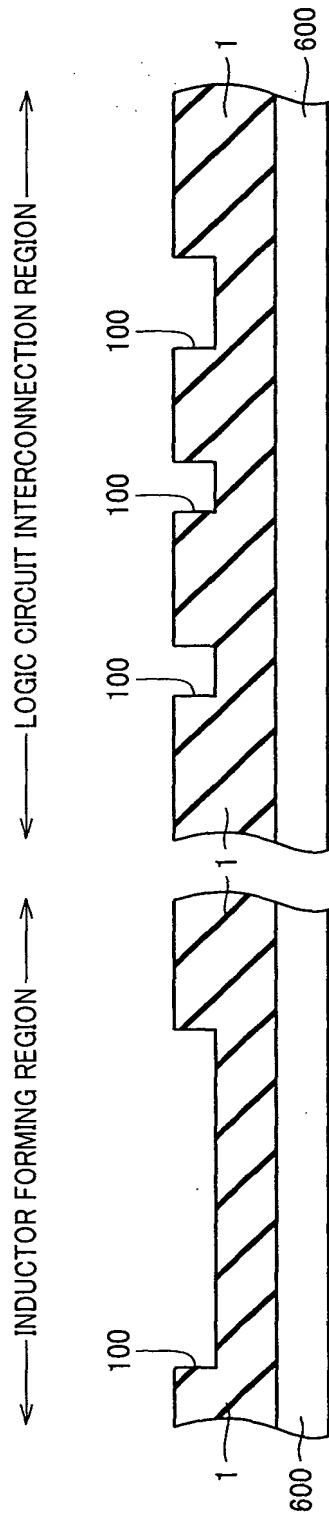


FIG.16

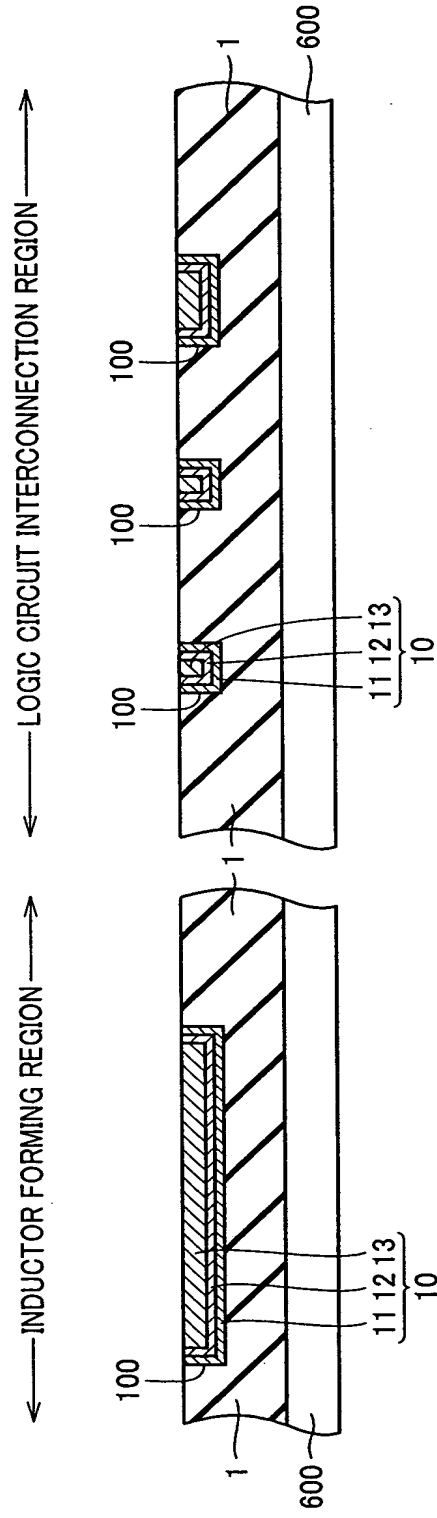




FIG.17

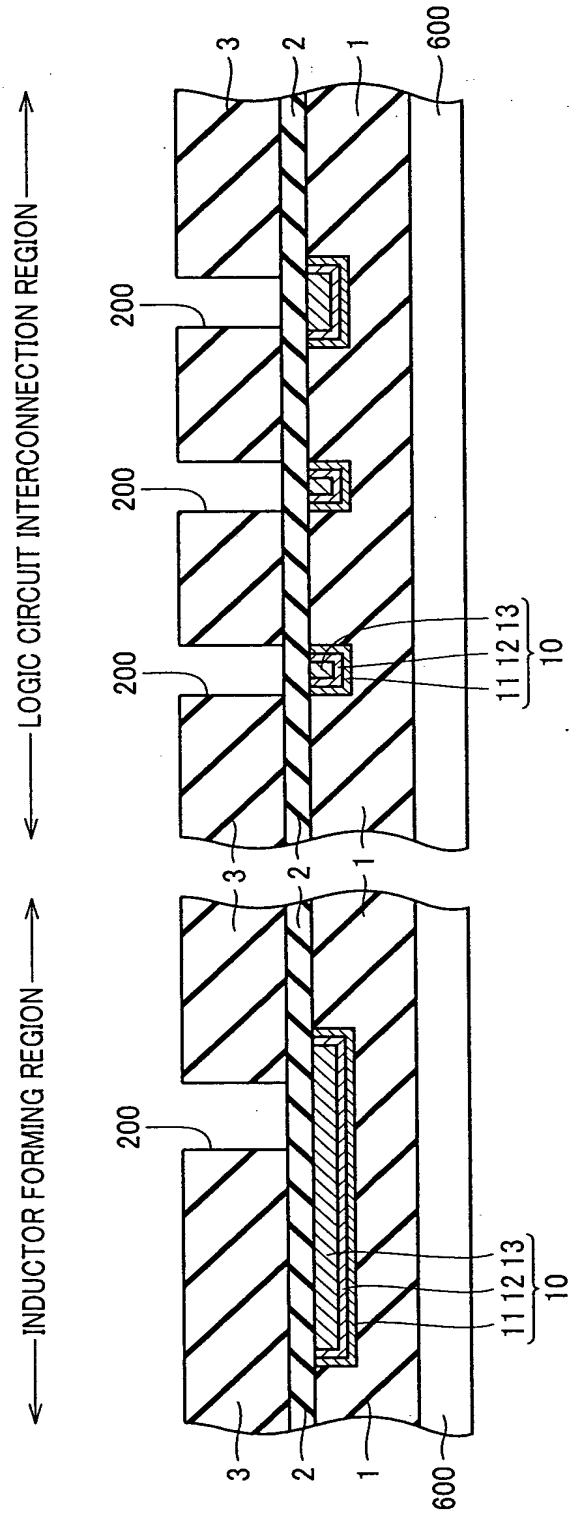


FIG.18

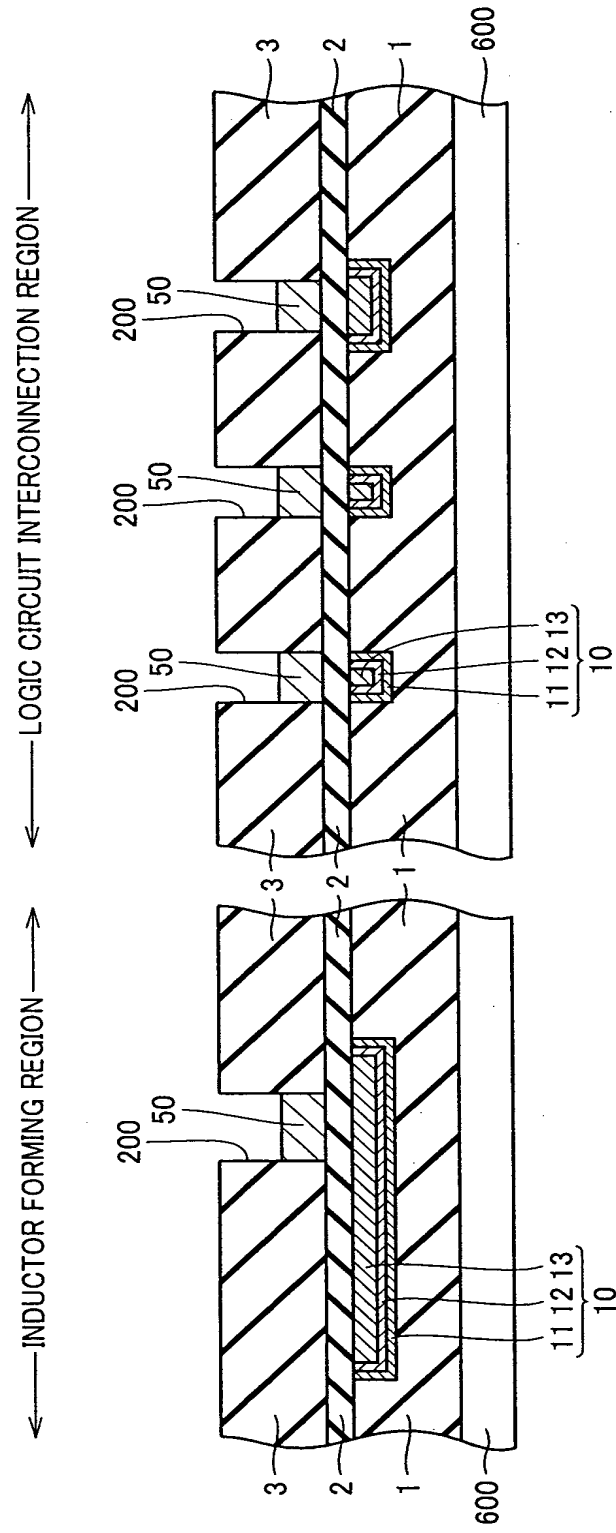


FIG.19

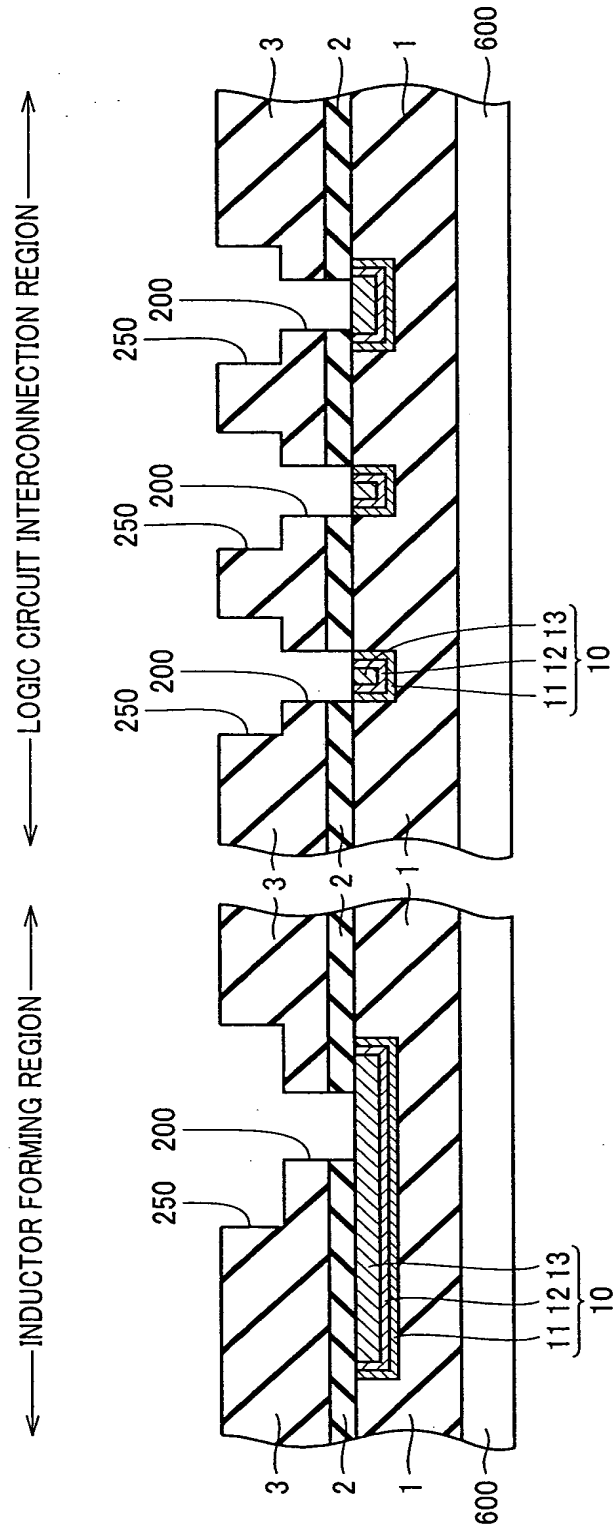


FIG.20

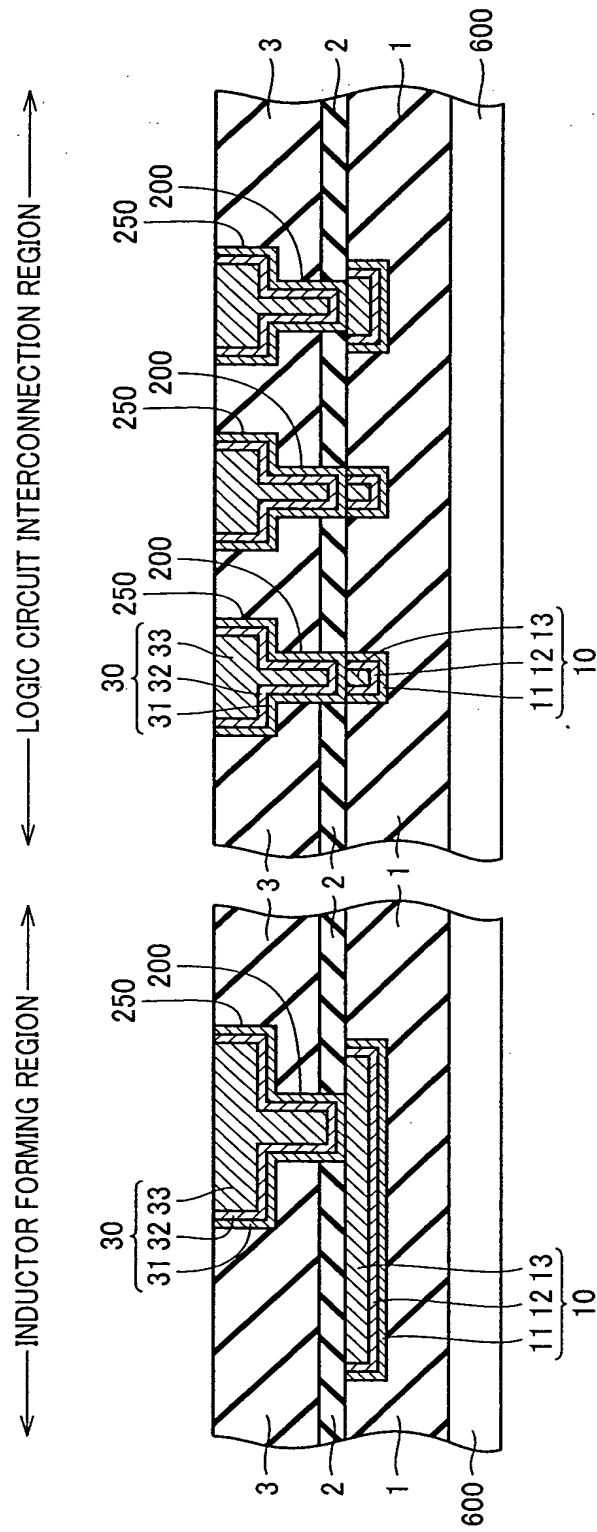


FIG.21

